Electronic Pater	nt App	lication Fee	Transmi	ttal			
Application Number:	108	10811835					
Filing Date:	30-	30-Mar-2004					
Title of Invention:	МЕ	METHOD OF DESIGNING A CIRCUIT LAYOUT OF A SEMICONDUCTOR DEVICE					
First Named Inventor/Applicant Name:	Kaz	Kazuaki Goto					
Filer:	Do	Donald R. Studebaker/Keiko Higuchi					
Attorney Docket Number:	030	030712-29					
Filed as Large Entity							
Utility under 35 USC 111(a) Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							

Publ. Fee- early, voluntary, or normal

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Printed copy of patent - no color	8001	3	3	9
	Tot	1819		